

In re Patent Application of:
LO IACONO
Serial No. 10/651,075
Filing Date: **AUGUST 28, 2003**

REMARKS

Applicant would like to thank the Examiner for the thorough examination of the present application, and for allowing Claims 71-74.

The Examiner rejected Claims 39-55 as being indefinite. In particular, the Examiner has taken the position that the claim recitation of "incrementing, decrementing or two's complementing a first string of N bits" is not supported by the current claims for providing the detailed steps/physical structure necessary to perform theses steps. The Examiner indicated that Claims 39-70 would be allowed upon overcoming the indefinite rejection.

Independent Claims 39 and 55 have been amended to more clearly define the present invention. In particular, the operation of incrementing, decrementing or two's complementing is determined by the second logic function and a choice of processing the first string or the negated replica thereof with the first logic combination.


The Applicant submits that the independent claims broadly recite generating the auxiliary string. Based on how the auxiliary string is generated determines if incrementing, decrementing or two's complementing of the N bit string is being performed when generating the output string. Accordingly, the Applicant submits that the claims are definite.

In view of the claim amendments and remarks provided herein, it is submitted that all the claims are patentable. Accordingly, a Notice of Allowance is requested in due course. Should any minor informalities need to be addressed, the Examiner is encouraged to contact the undersigned attorney at the

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Respectfully submitted,



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